



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent application of

Akio KITAMURA

Serial No.: 10/643,718

Group Art Unit: 2814

Filed: August 19, 2003

Examiner: Weiss, H.

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE (As Amended)

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450 on:

Date: 11/08/04
By: DR
Marc A. Rossi

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

In response to the Final Official Action issued on May 28, 2004, applicants have filed a Request for Continued Examination which is enclosed herewith. As the period for response expired on August 28, 2004, applicants hereby petition for a three month extension of time. The Commissioner is authorized to charge the extension fee in the amount of \$ 980.00 and any deficiency in fees or any additional fees required to maintain the pendency of this application to Deposit Account 18-2056.

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02 FC:1253 980.00 DA

11/08/04
Date

Respectfully submitted,

ROSSI & ASSOCIATES

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Attorney Docket: FUJI:213A